

This listing of claims will replace all prior versions, and listings, of claims in the application.

LISTING OF CLAIMS

1. (Currently Amended) ~~Method~~ A method for setting an operating parameter in a peripheral IC, the method comprising:

transmitting the operating parameter from a central IC via a bus connection to the peripheral IC, the bus connection being a serial bus connection having a data line, a control line, and a clock line;

buffering the operating parameter in a preregister of the peripheral IC, a current operating parameter being stored in a working register of the peripheral IC;

sending a start ~~signal~~ pulse signaling a start of a data transmission from the central IC to the peripheral IC via the control line; and

sending a transfer ~~signal~~ pulse from the central IC to the peripheral IC via the control line, the transfer ~~signal~~ pulse triggering transferring of the buffered operating parameter to the working register, wherein the buffered operating parameter becomes active in a working process of the peripheral IC;

wherein the start ~~signal~~ pulse is transmitted on the control line ~~with one of a rising and a falling edge of a system clock signal~~ during a first phase where ~~the system~~ transitions of the clock signal ~~is~~ are present on the clock line and wherein the transfer ~~signal~~ pulse is transmitted on the control line in a second phase where transitions of the system clock signal ~~is~~ are not present on the clock line.

2. (Cancelled).

3. (Cancelled).

4. (Currently Amended) ~~Method~~ The method according to claim 1, further comprising transferring a register write address for writing to the preregister in the peripheral IC on the data line ahead of the operating parameter.

5. (Cancelled).

6. (Currently Amended) A device for setting an operating parameter in a peripheral IC, the device comprising:

a serial bus connection between a central IC and the peripheral IC, the serial bus connection having a data line, a control line, and a clock line;

~~means for transmitting the operating parameter from the central IC to the peripheral IC via the serial bus connection;~~

a preregister for buffering the operating parameter of the peripheral IC;

a working register for storing a current operating parameter of the peripheral IC;

means for transmitting a transfer ~~signal~~ pulse from the central IC to the peripheral IC over the control line, the transfer ~~signal~~ pulse triggering transferring of the buffered operating parameter to the working register, wherein the buffered operating parameter becomes active in a working process of the peripheral IC; and

signaling means for transmitting a start ~~signal~~ pulse for data transmission from the central IC to the peripheral IC over the control line;

wherein the start ~~signal~~ pulse is transmitted on the control line ~~with one of a rising or a falling edge of a system clock signal during a first phase where the system transitions of the clock signal is are present on the clock line and~~ wherein the transfer signal pulse is transmitted on the control line in a second

phase where transitions of the ~~system~~ clock signal ~~is~~ are not present on the clock line.

7. (Cancelled).

8. (Cancelled).

9. (Currently Amended) ~~Device~~ The device according to claim 6, further including bus protocol means according to which a register write address for writing to the preregister is transferred to the peripheral IC on the data line ahead of the operating parameter.

10. (Cancelled).

11. (Currently Amended) ~~Device~~ The device according to claim 6, wherein the peripheral IC relates to a front-end IC for a communication arrangement for wireless data transmission and the central IC relates to a signal processing device, with means for one of modulation and demodulation of a mixed RF input signal and for further signal processing in baseband.

12. (Currently Amended) ~~Device~~ The device according to ~~as claimed in~~ claim 11, wherein the operating parameter relates to a gain setting for a receive gain in the front-end IC.

13. (Currently Amended) ~~Device~~ The device according to claim 6, wherein the device is configured as a send and receive device for wireless data transmission in accordance with the HIPERLAN2 standard.